


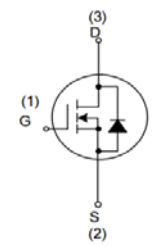
N-Channel Enhancement-Mode Power MOSFET (30V, 80A)

PRODUCT SUMMARY

V_{DSS}	I_D	$R_{DS(on)}$ (m Ω)Typ
30V	80A	4.5@ $V_{GS} = 10V, I_D=40A$
		5.5@ $V_{GS} = 4.5V, I_D=20A$

Features

- Super high dense cell trench design for low $R_{DS(on)}$
- Rugged and reliable
- TO-252-2L package
- Lead (Pb) -free and halogen-free

	<p>ET3080 Pin Assignment & Symbol</p> <p>3-Lead Plastic TO-252-2L</p> <p>Pin 1: Gate Pin 2: Source Pin3: Drain</p>	
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Absolute Maximum Ratings ($T_A=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Ratings	U nit
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current (Continuous)	80	A
	Drain Current (Continuous)($T_C=100^\circ\text{C}$)	50	A
I_{DM}	Drain Current (Pulsed) ^a	320	A
EAS	Single pulse avalanche energy	300	mJ
P_D	Total Power Dissipation @ $T_A=25^\circ\text{C}$	110	W
	Total Power Dissipation @ $T_A=75^\circ\text{C}$	85	W
T_j, T_{stg}	Operating Junction and Storage Temperature Range	-55 to +150	$^\circ\text{C}$
R_{QJA}	Thermal Resistance Junction to Ambient (PCB mounted) ^b	0.48	$^\circ\text{C}/\text{W}$

a: Repetitive Rating: Pulse width limited by the maximum junction temperature.

b: 1-in² 2oz Cu PCB board

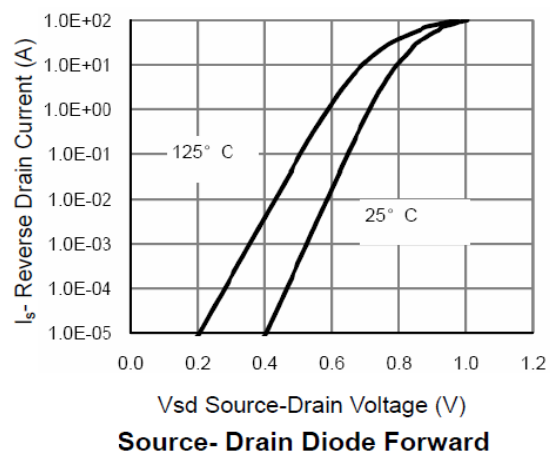
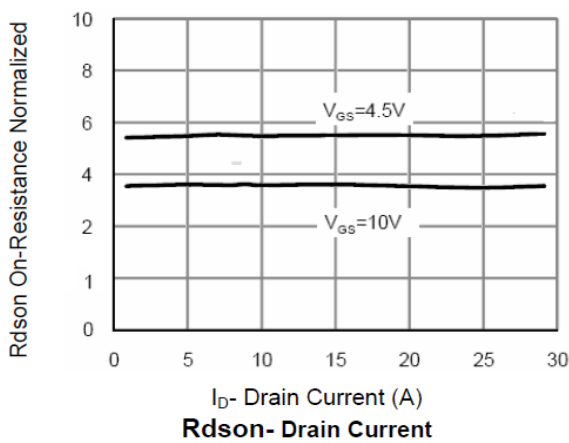
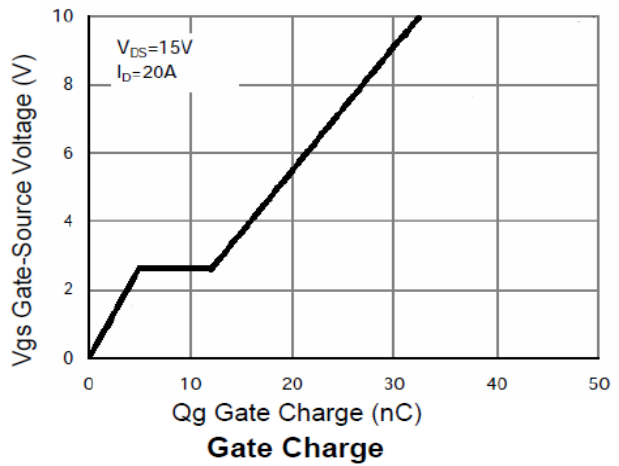
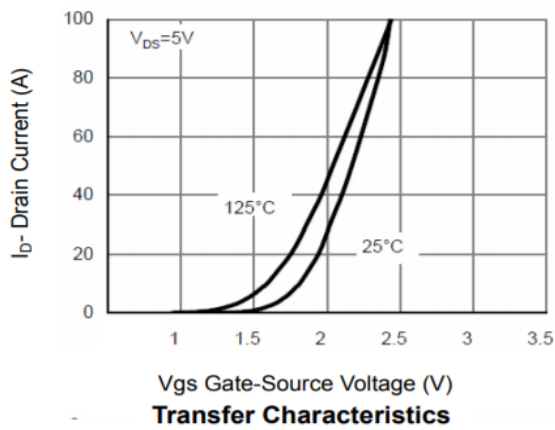
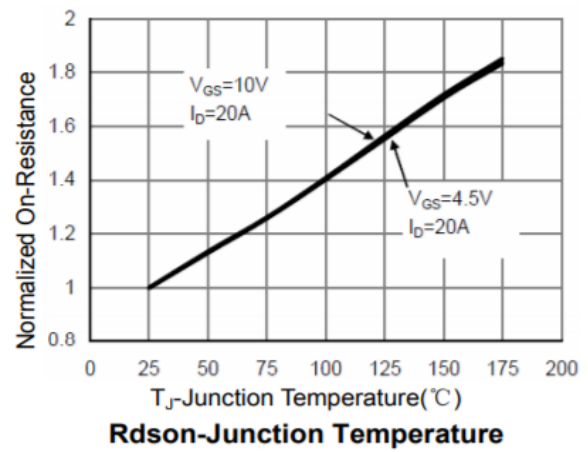
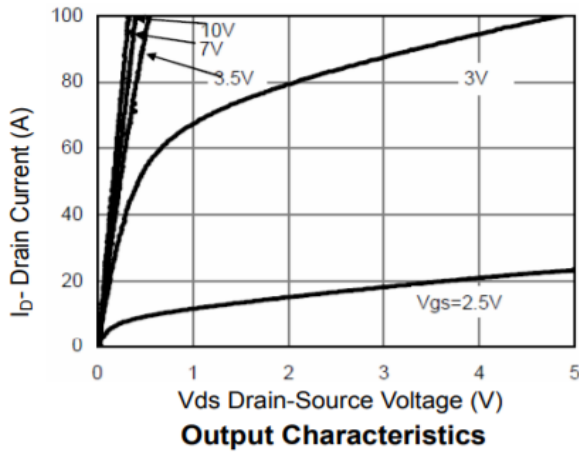


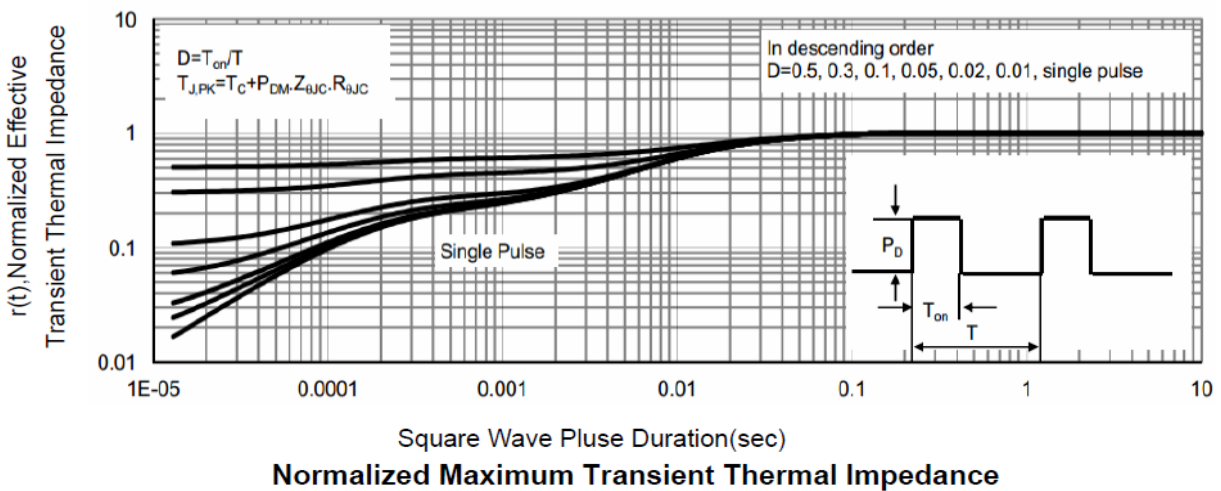
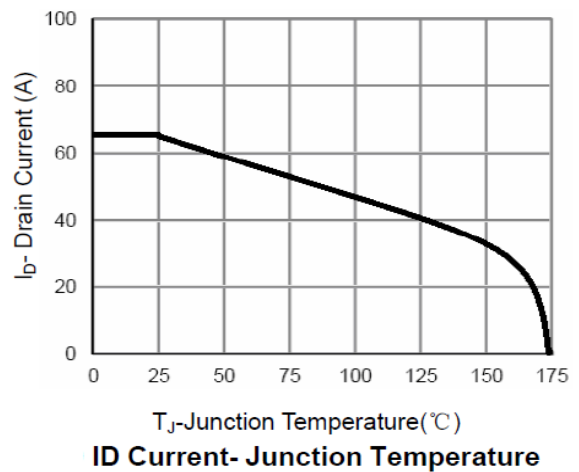
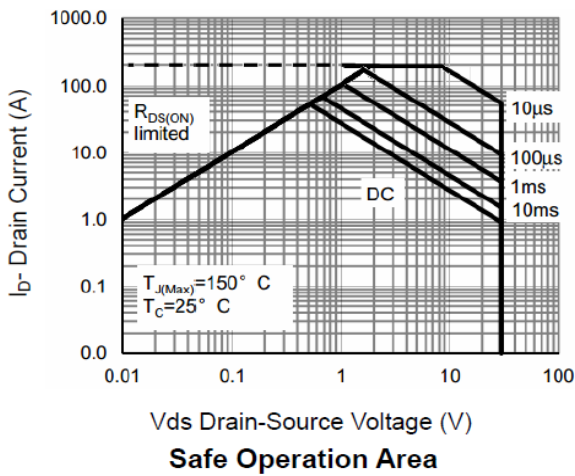
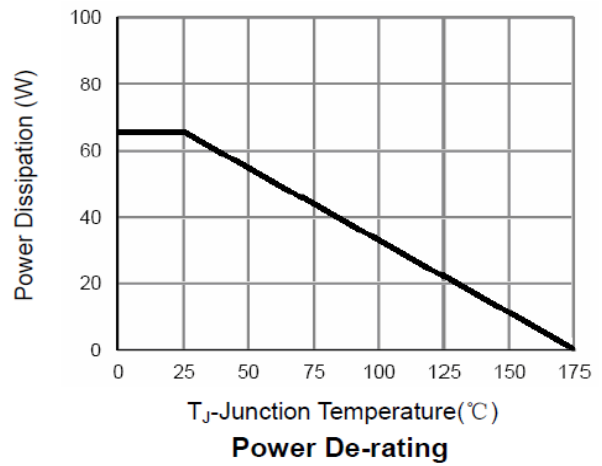
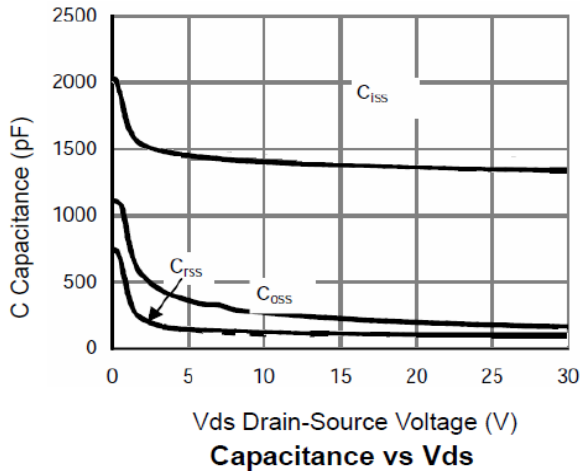
Electrical Characteristics (T_A=25°C, unless otherwise noted)

Symbol	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
• Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =30V, V _{GS} =0V	-	-	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
• On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250uA	1	1.6	2.5	V
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =20A	-	4.5	6	mΩ
		V _{GS} =4.5V, I _D =15A		5.5	7.5	Ω
g _{FS}	Forward Transconductance	V _{DS} =15V, I _D =20A	50			S
I _s	Maximum Body-Diode Continuous Current				80	A
• Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	-	1400	-	PF
C _{oss}	Output Capacitance		-	205	-	
C _{rss}	Reverse Transfer Capacitance		-	170	-	
R _g	Gate resistance	V _{DS} =15V, V _{GS} =0V, f=1MHz		2.2		Ω
• Switching Characteristics						
Q _g	Total Gate Charge	V _{DS} =15V, I _D =20A, V _{GS} =10V	-	32	-	nC
Q _{gs}	Gate-Source Charge		-	5	-	
Q _{gd}	Gate-Drain Charge		-	6.5	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} =15V, R _L =1.25Ω V _{GS} =4.5V, R _G =3Ω	-	9	-	nS
t _r	Turn-on Rise Time		-	8	-	
t _{d(off)}	Turn-off Delay Time		-	28	-	
t _f	Turn-off Fall Time		-	5	-	
• Drain-Source Diode Characteristics						
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} =0V, I _S =-1A	-	0.7	1	V

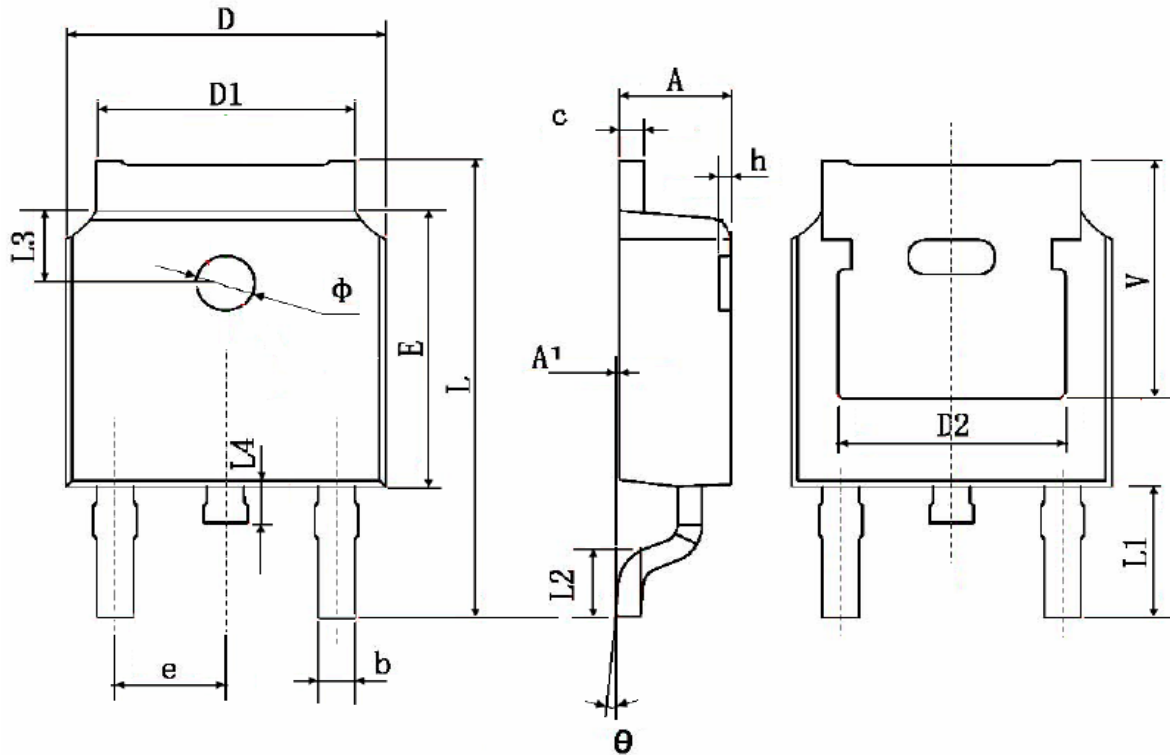
Note: Pulse Test: Pulse Width ≤ 300us, Duty Cycle ≤ 2%

Typical Characteristics Curves (Ta=25°C, unless otherwise note)





TO-252-2L PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	